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XA-10578

PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

FUKUDA, Keiko et al.

Intl. Appln. No.: PCT/JP2004/017910

Intl. Filing Date: 2 December 2004

For: VOLTAGE GENERATION CIRCUIT AND SEMICONDUCTOR  
INTEGRATED CIRCUIT DEVICE

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PRELIMINARY AMENDMENT

Commissioner for Patents  
P.O. Box 1450  
Alexandria, Virginia 22313-1450

Attn: Mail Stop PCT, DO/EO/US

Sir:

Prior to examination, please amend the above-  
identified patent application as indicated below.

Amendments to the claims are reflected in the listing  
of claims which begin on page 2 of this paper.

Remarks begin on page 13 of this paper.